GLF74138



4.5 A Power Mux IC with Bidirectional Operation and Low Power Consumption

Product Specification

DESCRIPTION

The GLF74138 is a fully integrated power path switches with the automatic and manual selection function.

The EN pin can be used along with the SEL pin to control two integrated main FETs of the GLF74138. By the combination of these two pins, one of input source selection modes is set to provide power to downstream system seamlessly. Each FET of the GLF74138 is conducted bidirectionally when it is turned on and current flows from VOUT to VIN pin and vice versa.

The automatic selection mode chooses a higher input voltage source between two inputs. In the manual selection mode, one of input sources is connected to downstream system.

FEATURES

- Two-Input and Single-Output Power Multiplexer Switch
- Automatic and Manual Input Selection Modes
- No Cross Conduction between Two Input Sources
- Bidirectional Current Flow at Conduction State
- Reverse Current Blocking when Disabled
- Supply Voltage Range: 2.0 V to 5.5 V
- R_{ON}: 20 mΩ Typ at 5.5 V_{IN1} or V_{IN2}
- 4.5 A Continuous Output Current Capability Per Channel
- Ultra-Low Supply Current at Operation
 I_Q: 4 µA Typ at 5.5 V_{IN}
- Ultra-Low Stand-by Current
 I_{SD}: 30 nA Typ at 5.5 V_{IN}
- Smart Control Pins

 I_{EN} and I_{SEL} : 10 nA Typ at V_{EN} or $V_{SEL} > V_{IH}$

 R_{EN} and R_{SEL} : 500 k Ω Typ

HBM: 6 kV. CDM: 2 kV

APPLICATIONS

- Smart Devices
- Subsystem with Backup Power
- IoT Tracking System

PACKAGE



VIN1	VIN1	VIN1		VIN1	VIN1	VIN1	
(A1)	(A2)	(A3)		(A3)	(A2)	(A1)	
VOUT	VOUT	VOUT		VOUT	VOUT	VOUT	
(B1)	(B2)	(B3)		(B3)	(B2)	(B1)	
VIN2	VIN2	VIN2		VIN2	VIN2	VIN2	
(C1)	(C2)	(C3) EN		(C3)	(C2)	(C1)	
SEL	GND			EN	GND	SEL	
(D1)	(D2)	(D3)		(D3)	(D2)	(D1)	
		\					
TOP VIEW				BOTTOM VIEW			

1.27 mm x 1.67 mm x 0.55 mm, WLCSP 0.4 mm pitch

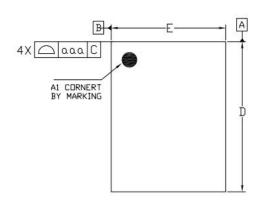
DEVICE INFORMATION

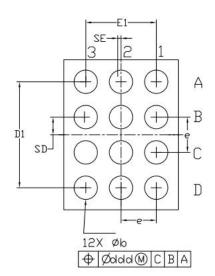
Part Number	Top Mark	Ron at 5.5 Vin	Output Current, I _{OUT} Per Channel	Ultra-low I _Q at 5.5 V _{IN}	Output Discharge	Status	
GLF74136	TBD	20 mΩ	4.5 A	4 µA	70 Ω	On request	
GLF74138	EN	20 mΩ	4.5 A	4 μΑ	NA	Released	

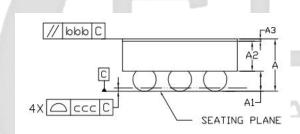


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PACKAGE OUTLINE

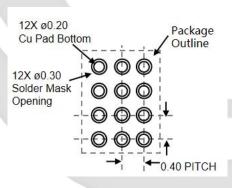






	Dimens	ional R	ef.				
REF.	Min.	Nom.	Max.				
Α	0.500	0.550	0.600				
Α1	0.175	0.200	0.225				
A2	0.300	0.325	0.350				
Α3	0.020	0.025	0.030				
D	1.655	1.670	1.685				
Е	1.255	1.270	1.285				
D1	1.150	1.200	1.250				
E1	0.750	0.800	0.850				
Ь	0.215	0.265	0.315				
е	e 0.400 BSC						
SD	0.200 BSC						
SE 0.000 BSC							
Tol. of Form&Position							
aaa	a 0.10						
ььь	0.10						
ccc	0.05						
ddd	0.05						

Recommended Footprint



Notes

- 1. ALL DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGRESS)
- 2. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M-1994.
- 3. A3: BACKSIDE LAMINATION

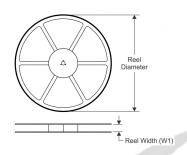


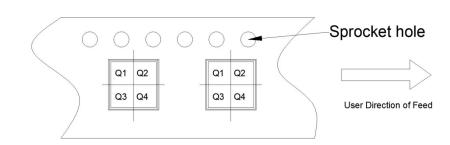
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TAPE AND REEL INFORMATION

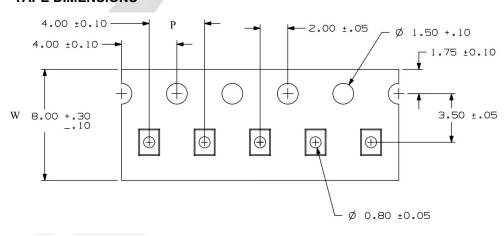
REEL DIMENSIONS

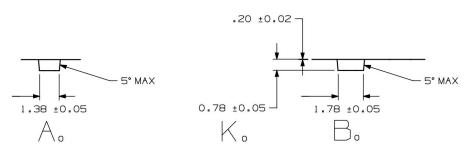
QUADRANT ASSIGNMENTS PIN 1 ORIENTATION TAPE





TAPE DIMENSIONS





Device	Package	Pins	SPQ	Reel Diameter (mm)	Reel Width W1	Α0	В0	K0	Р	W	Pin1
GLF74136	WLCSP	12	3000	180	9	1.38	1.78	0.78	4	8	Q1
GLF74138	WLCSP	12	3000	180	9	1.38	1.78	0.78	4	8	Q1

Remark:

- A0: Dimension designed to accommodate the component width
- B0: Dimension designed to accommodate the component length
- C0: Dimension designed to accommodate the component thickness
- W: Overall width of the carrier tape
- P: Pitch between successive cavity centers