

DESCRIPTION

The GLF1521 is an integrated load switch with a programmable slew rate control to provide variable output voltage rising times by an external capacitor to minimize inrush current.

The GLF1521 operates in an input range from 0.6 V to 3.6 V and supports 5 A maximum continuous output current. The GLF1521 features a low R_{ON} , quiescent current (I_Q) and shutdown current (I_{SD}), which support implementation of better system efficiency.

The GLF1521 is available in 2 mm x 2 mm DFN package with a thermal pad on the bottom for high power dissipation.

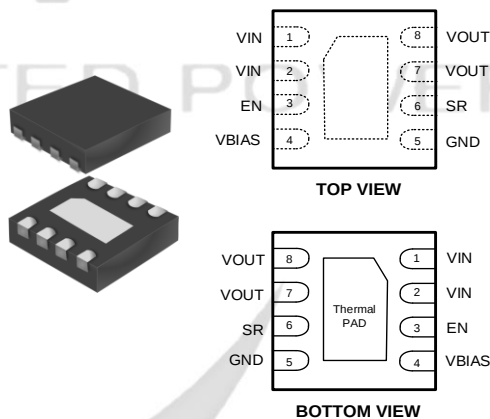
APPLICATIONS

- Solid-State Drives
- Notebooks
- Telecom Systems

FEATURES

- Input Voltage Range, V_{IN} : 0.6 V to 3.6 V
- Supply voltage range, V_{BIAS} : 2.5 V to 3.6 V
- Ultra-Low On-Resistance, R_{ON} : 22 m Ω
- 5 A Maximum Continuous Output Current
- Low Quiescent Current, I_{Q_BIAS} :
15 μ A at $V_{BIAS} = 3.3$ V
- Reverse Current Blocking during off state
- Built-In Thermal Shutdown
- Programmable VOUT Rise Time
- Internal EN Pull-Down Resistor
- Output Discharge Switch

PACKAGE

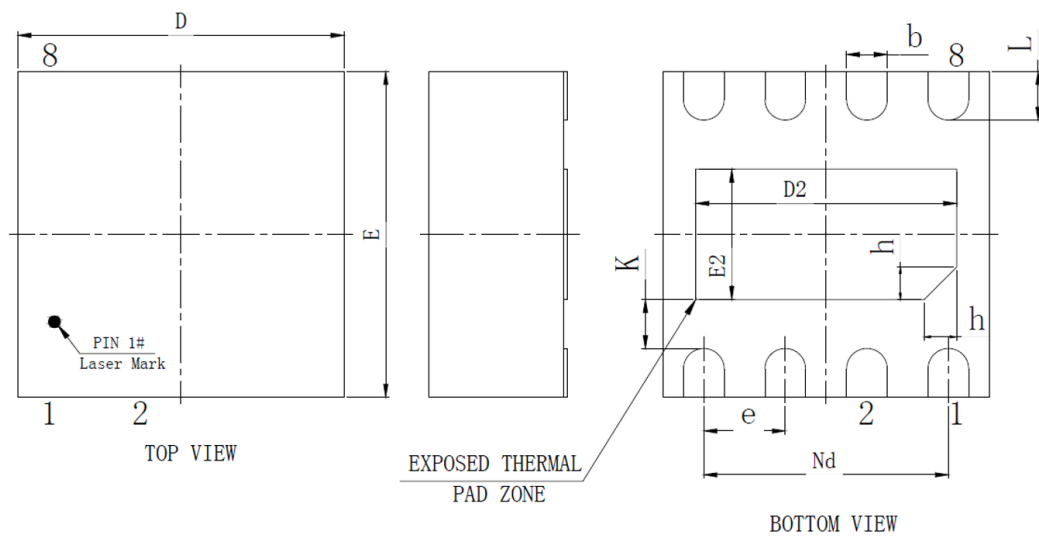


2 mm x 2 mm DFN-8L

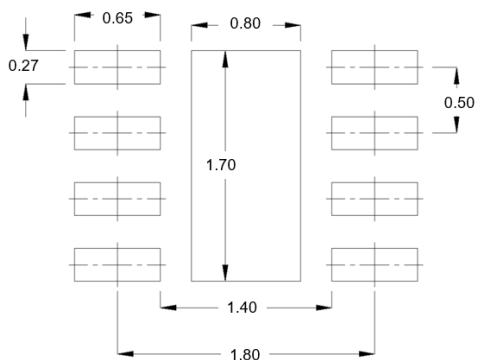
DEVICE ORDERING INFORMATION

Part Number	Top Mark	R_{ON} (Typ.)	Output Discharge	EN Activity	Package
GLF1521-D2G7	HH	22 m Ω	200 Ω	High	2 mm x 2 mm DFN-8L

PACKAGE OUTLINE



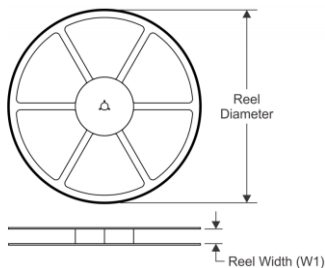
Recommended Footprint



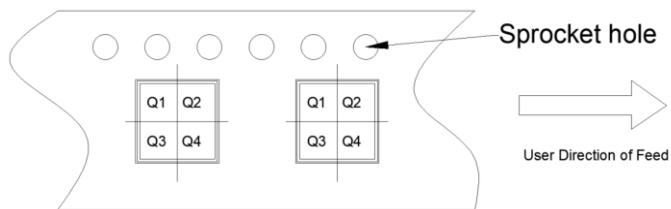
Symbol	Dimensions in Millimeters		
	MIN.	NOM.	MAX.
A	0.80	0.85	0.90
	0.70	0.75	0.80
A1	-	0.02	0.05
b	0.20	0.25	0.30
c	0.203REF		
D	1.95	2.00	2.05
D2	1.55	1.60	1.65
Nd	1.50BSC		
e	0.50BSC		
E	1.95	2.00	2.05
E2	0.75	0.80	0.85
L	0.25	0.30	0.35
K	0.25	0.30	0.35
h	0.20REF		

TAPE AND REEL INFORMATION

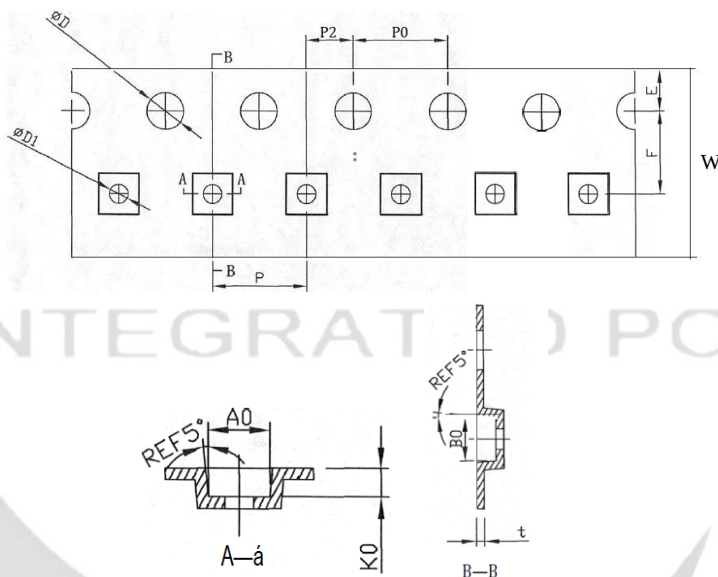
REEL DIMENSIONS



QUADRANT ASSIGNMENTS PIN 1 ORIENTATION TAPE



TAPE DIMENSIONS



Device	Package	Pins	SPQ	Reel Diameter (mm)	Reel Width W1	A0	B0	K0	P	W	Pin1
GLF1521-D2G7	DFN2x2	8	3000	178	8.6	2.15	2.15	0.88	4	8	Q1

Remark:

- A0: Dimension designed to accommodate the component width
- B0: Dimension designed to accommodate the component length
- C0: Dimension designed to accommodate the component thickness
- W: Overall width of the carrier tape
- P: Pitch between successive cavity centers